

FIG. 1

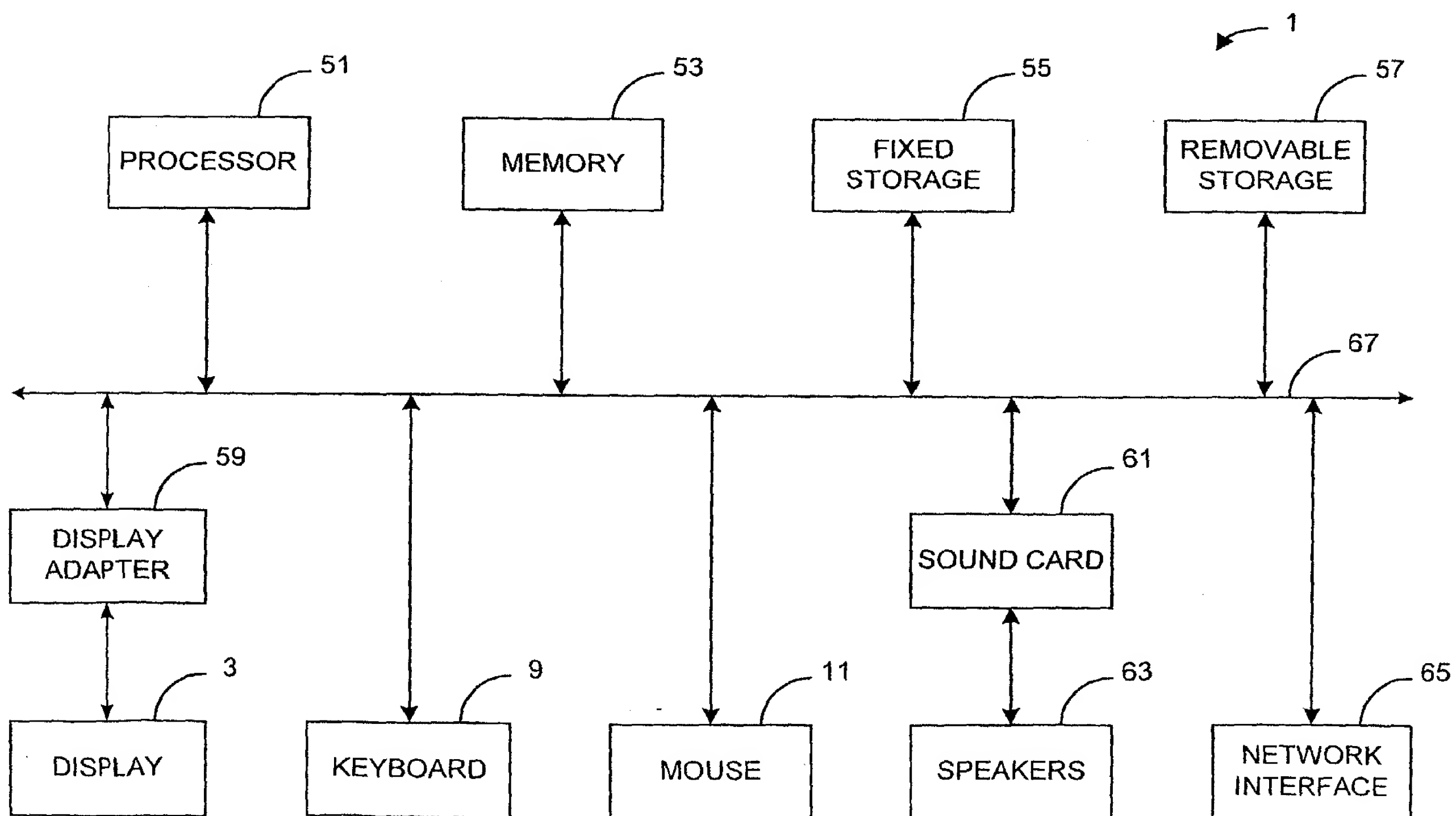


FIG. 2

FIG. 3 is a block diagram of a system for generating a mask design for a semiconductor device. The system includes a database 102, a CPU 100, a chip design file(s) 104, a mask design 108, a mask making 106, a synthesizer 110, a scanner 120, an image file(s) 124, an analysis CPU 126, and a gene sequence mutation detection 128. The system is configured to generate a mask design for a semiconductor device based on a desired character-istics and a database.

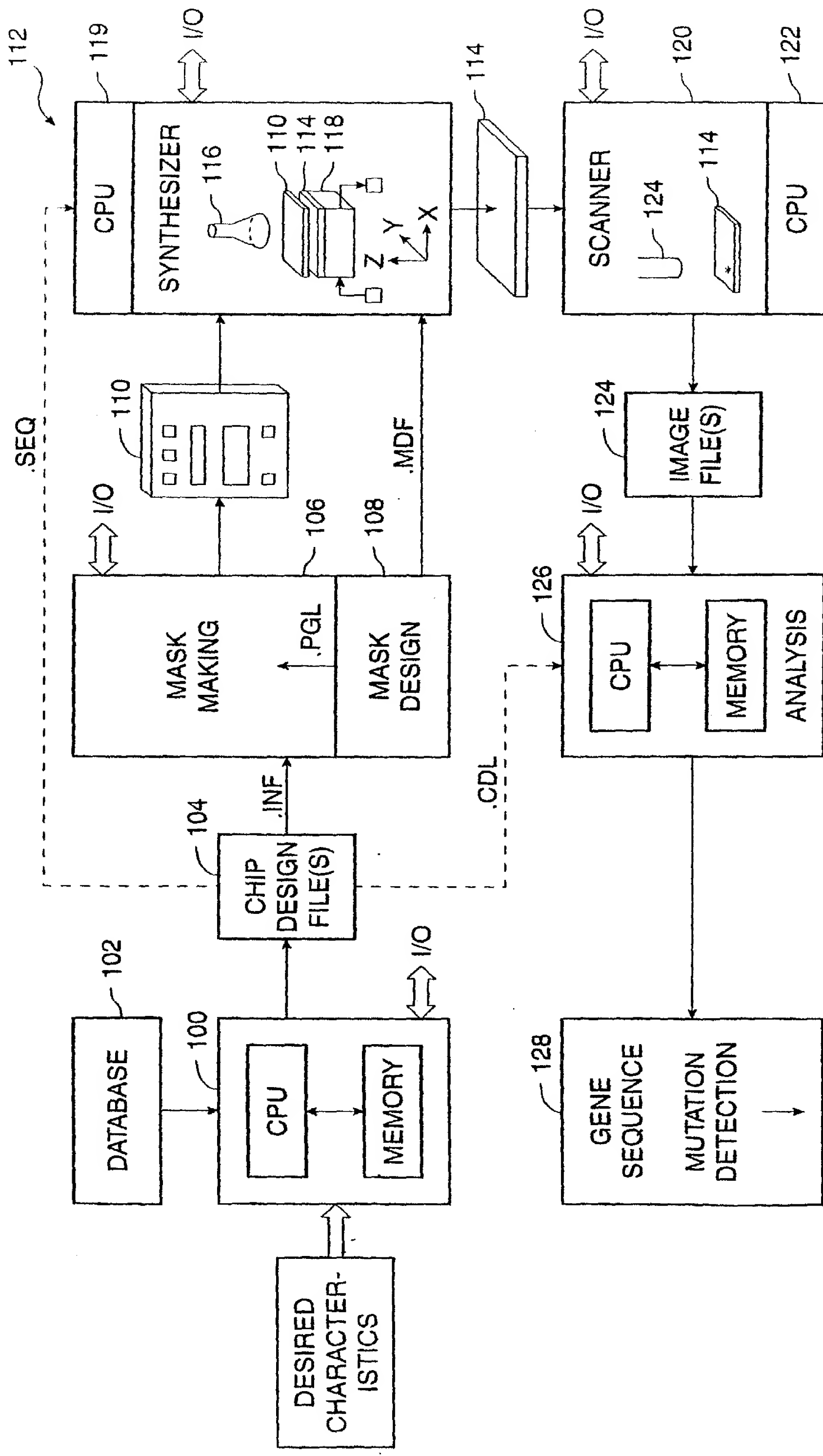


FIG. 3

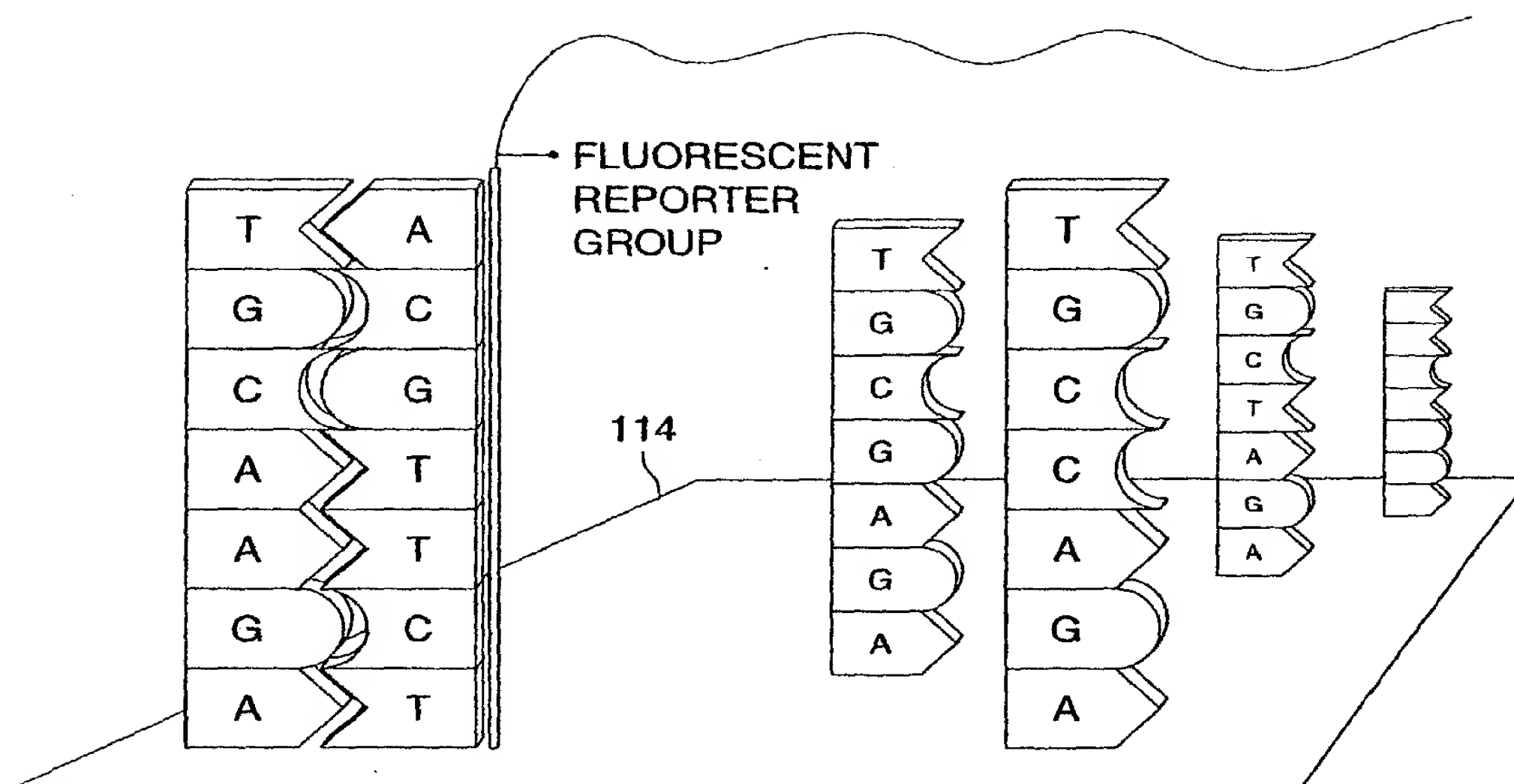


FIG. 4

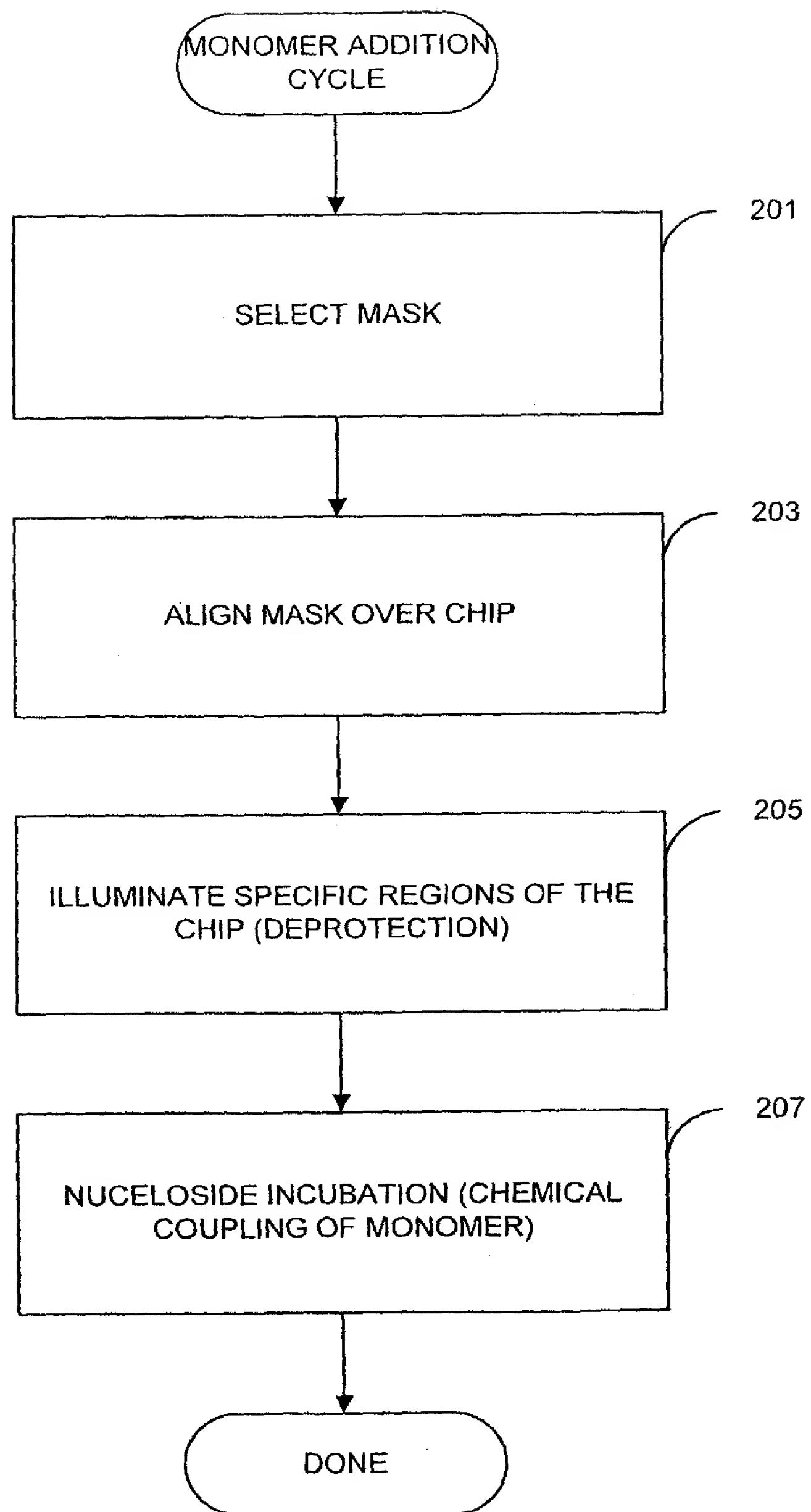


FIG. 5

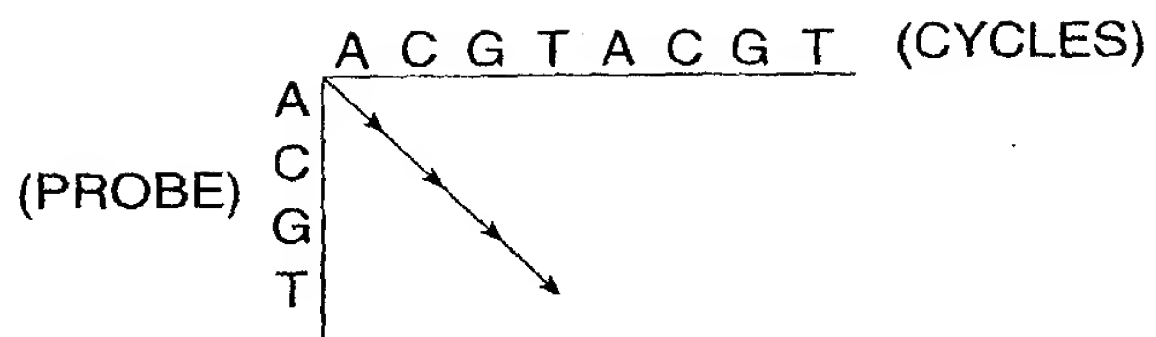


FIG. 6A

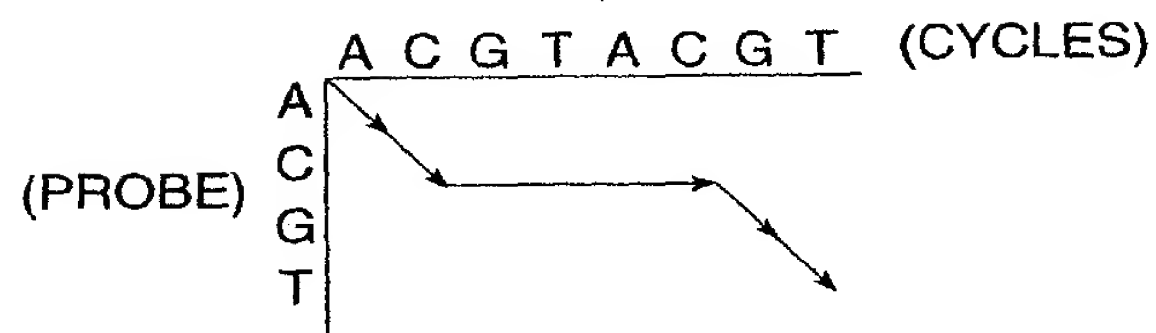


FIG. 6B

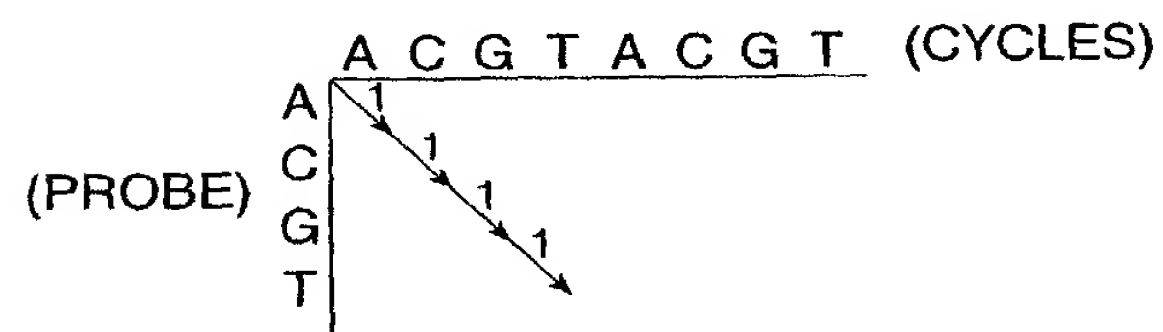


FIG. 7A

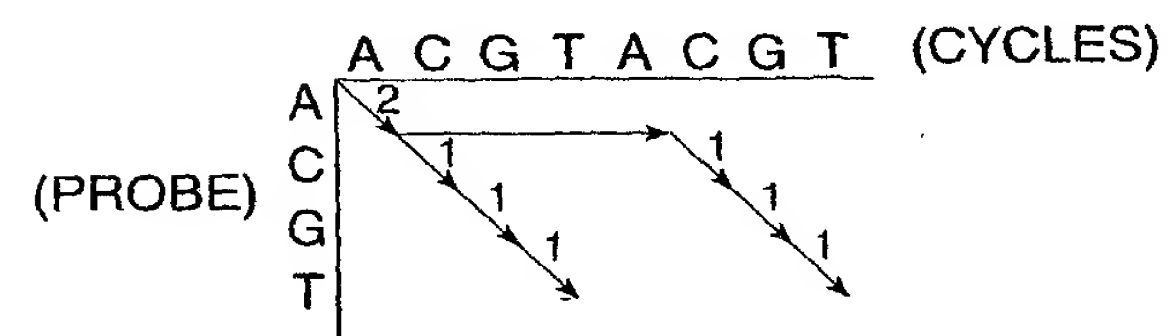


FIG. 7B

	1	2	3	4	5	6	7	8
1	X	1	1	1	0	1	1	1
2	1	X	0	0	0	1	1	1
3	1	0	X	0	0	1	1	1
4	1	0	0	X	0	1	1	1
5	0	0	0	0	X	0	0	0
6	1	1	1	1	0	X	0	0
7	1	1	1	1	0	0	X	0
8	1	1	1	1	0	0	0	X

FIG. 8

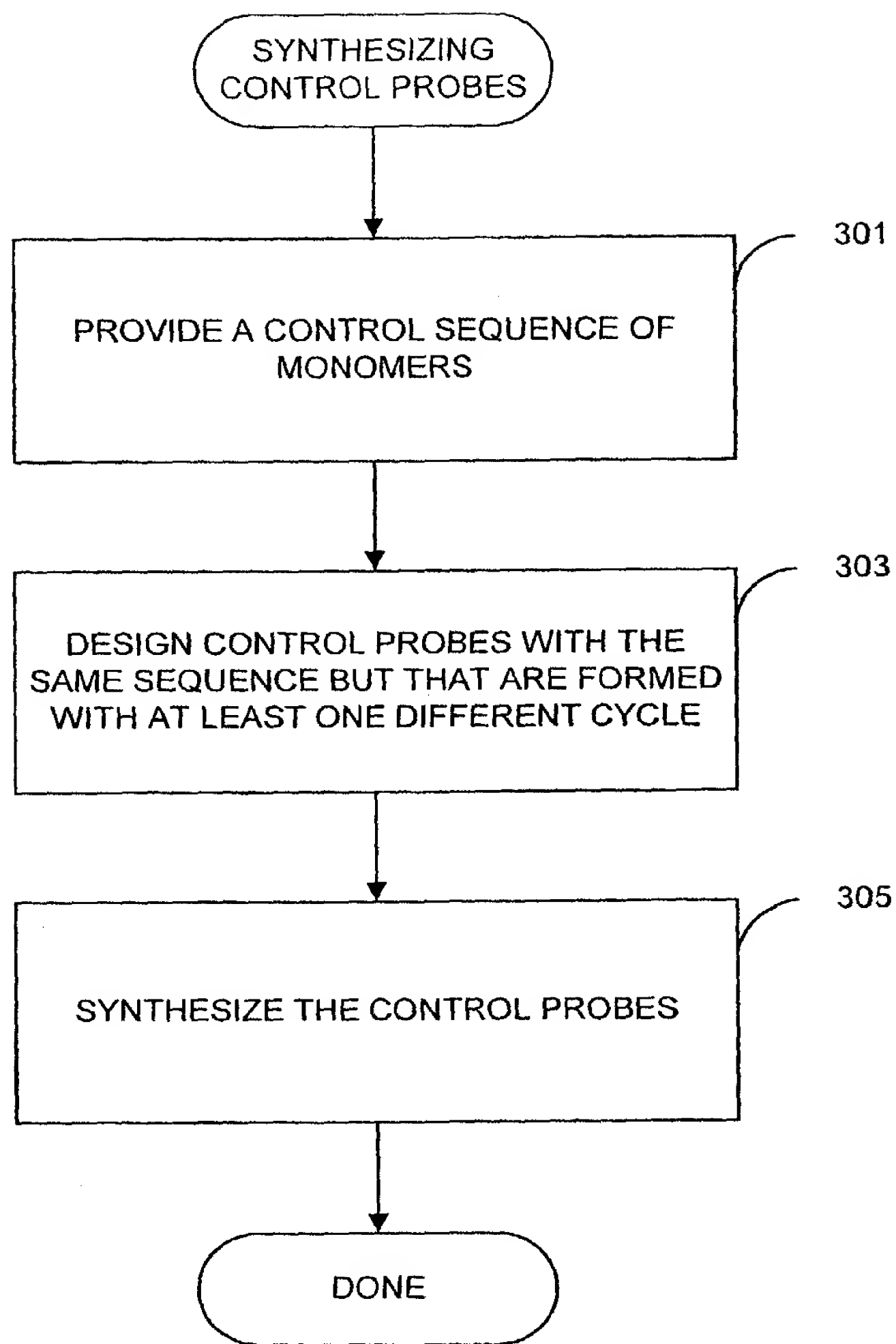


FIG. 9

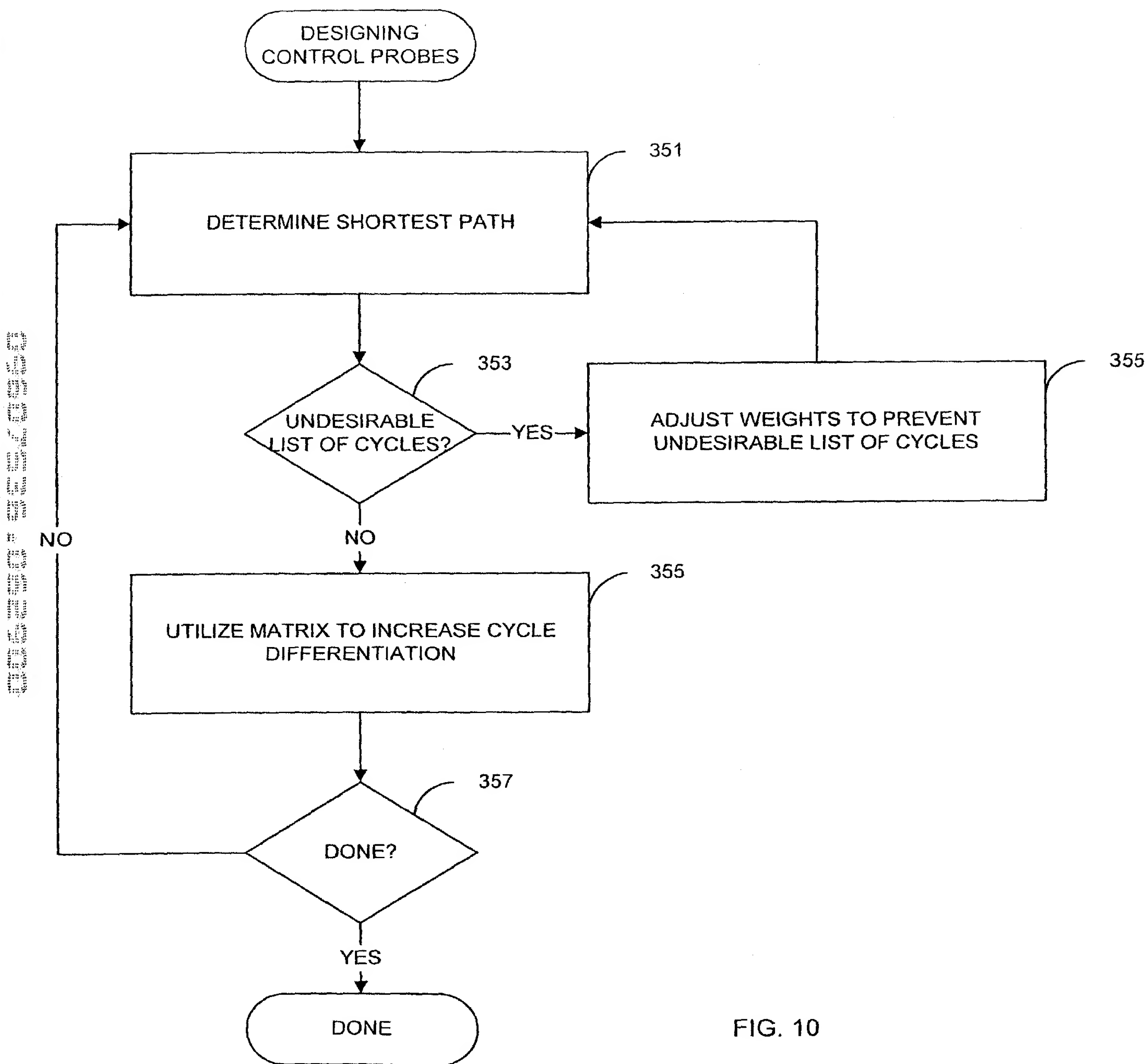


FIG. 10

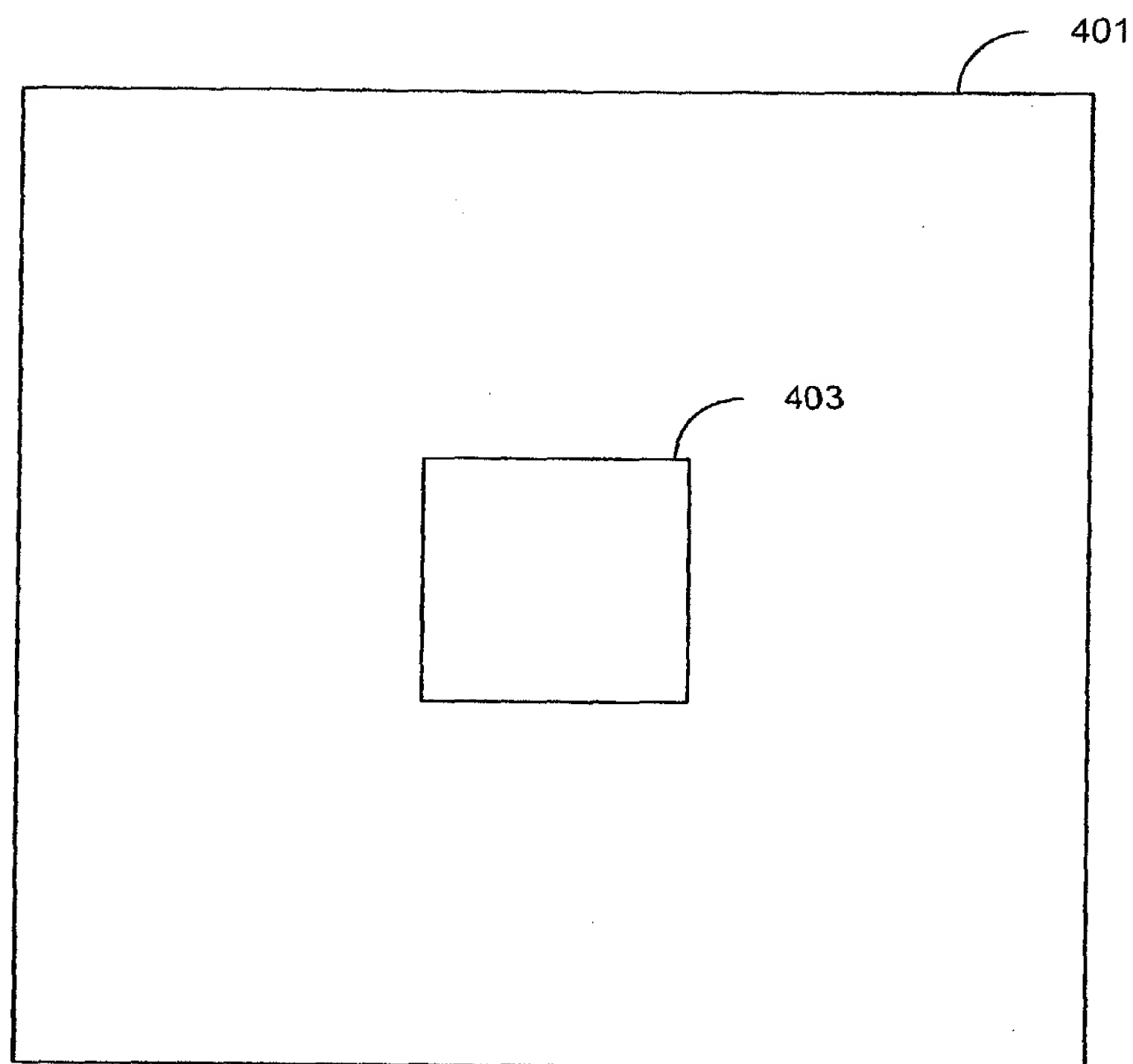


FIG. 11A

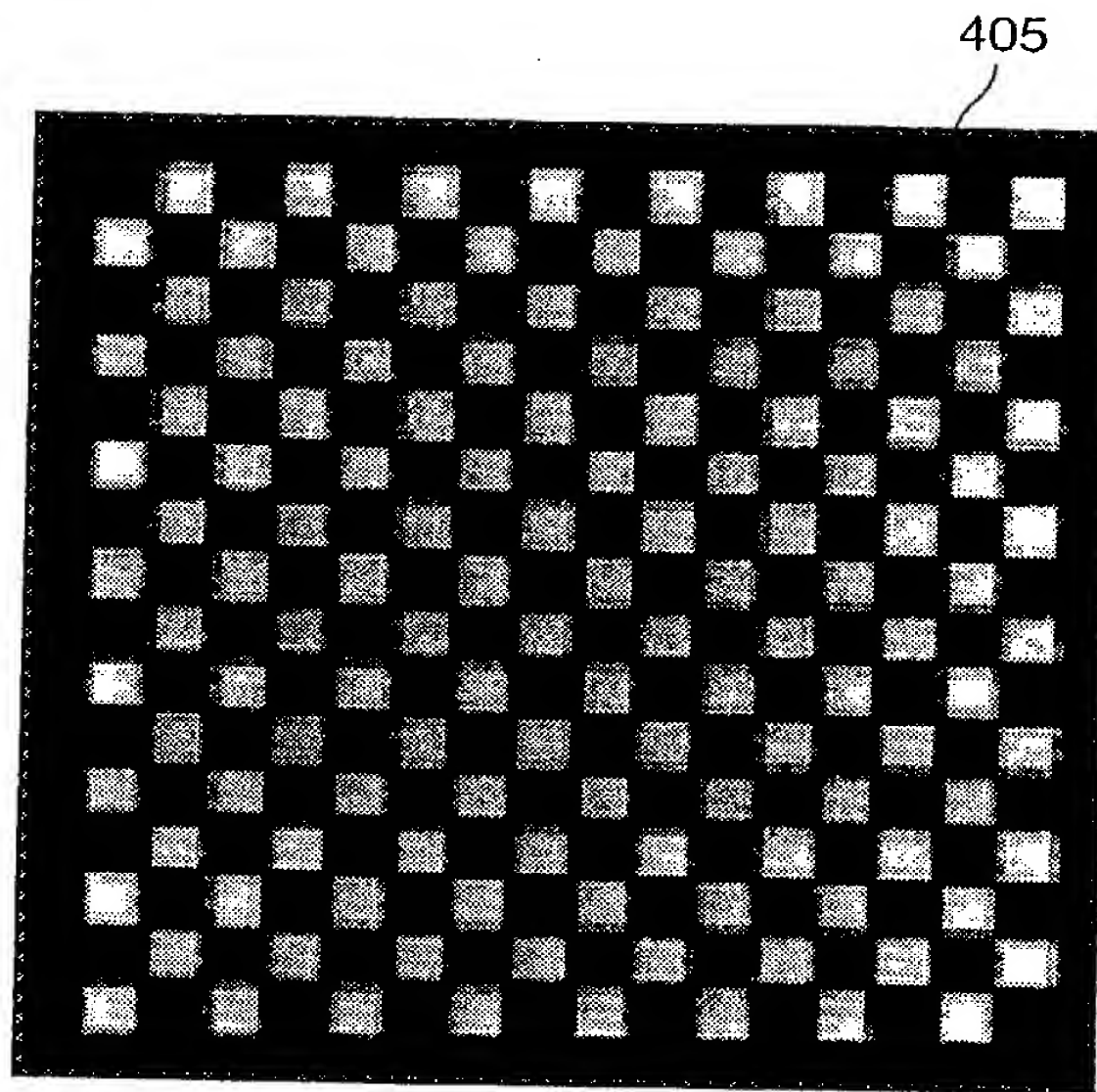


FIG. 11B

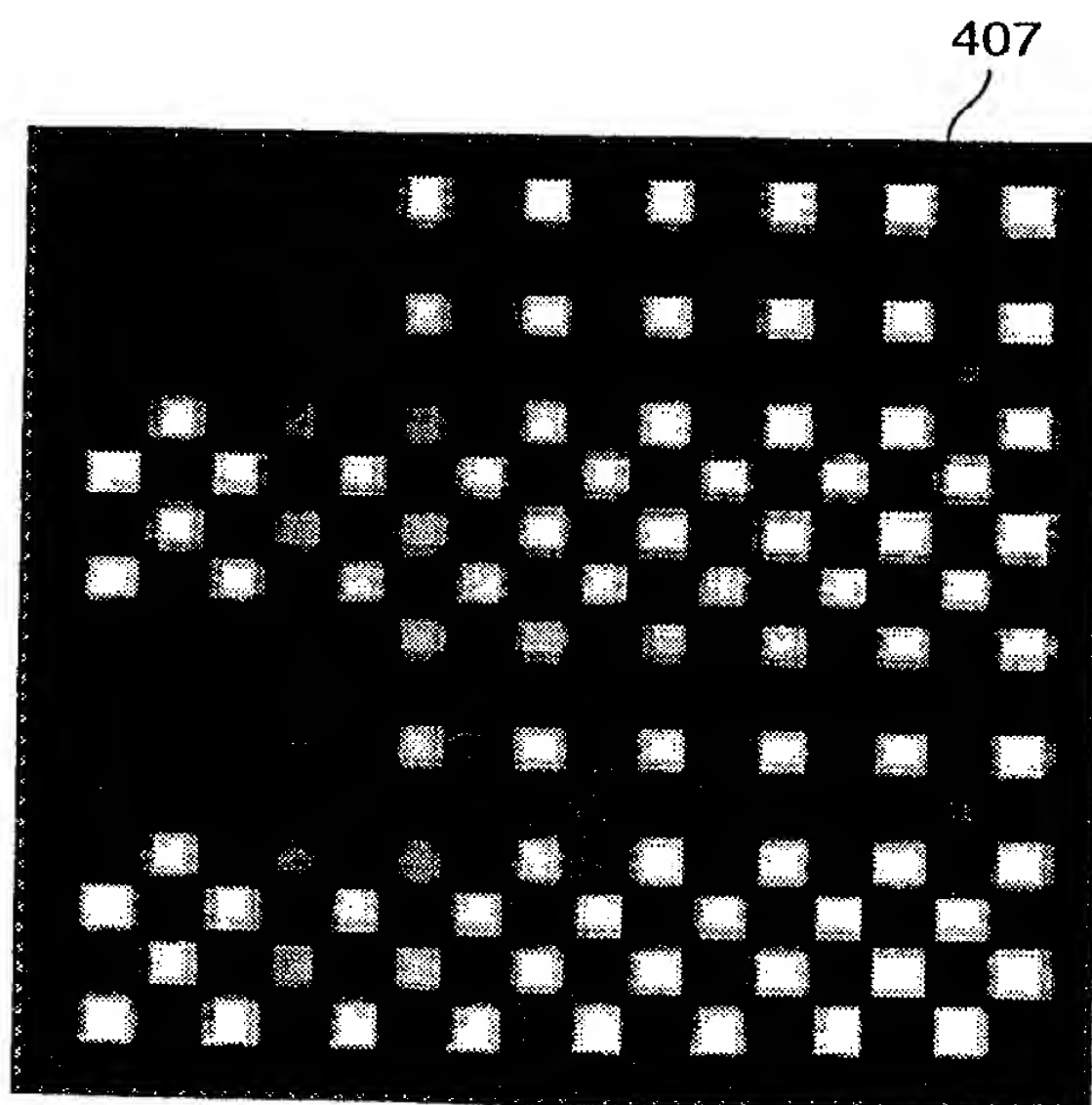


FIG. 11C

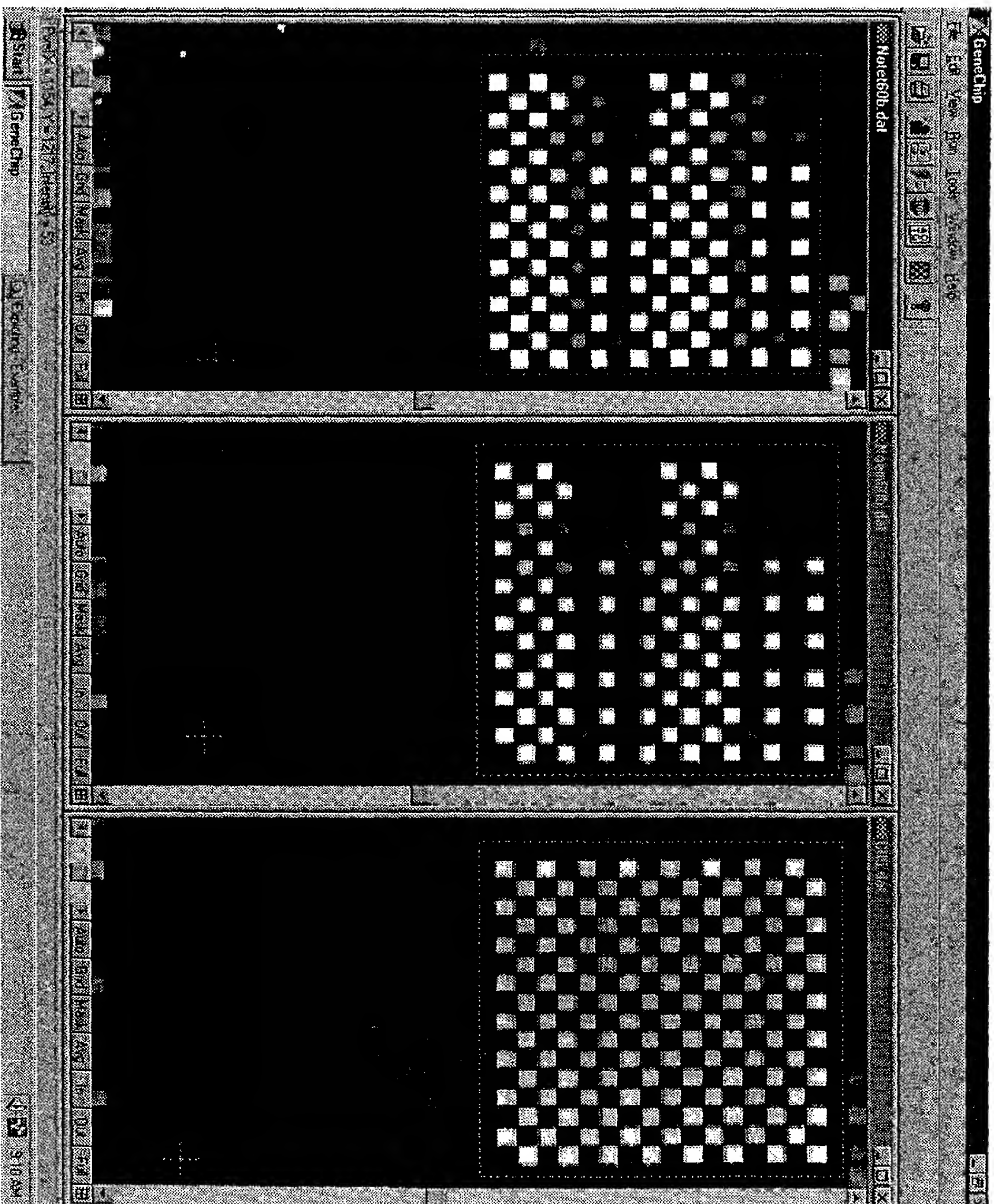


FIG. 12

FIG. 12 is a schematic diagram of a microarray chip. The chip is divided into three panels, each displaying a grid of white squares on a black background, representing data points. The interface includes a menu bar at the top with options like 'File', 'Edit', 'View', 'Data', 'Tools', 'Window', and 'Help'. A toolbar with various icons is located below the menu bar. The status bar at the bottom shows 'GeneChip' and 'Note60b del'.

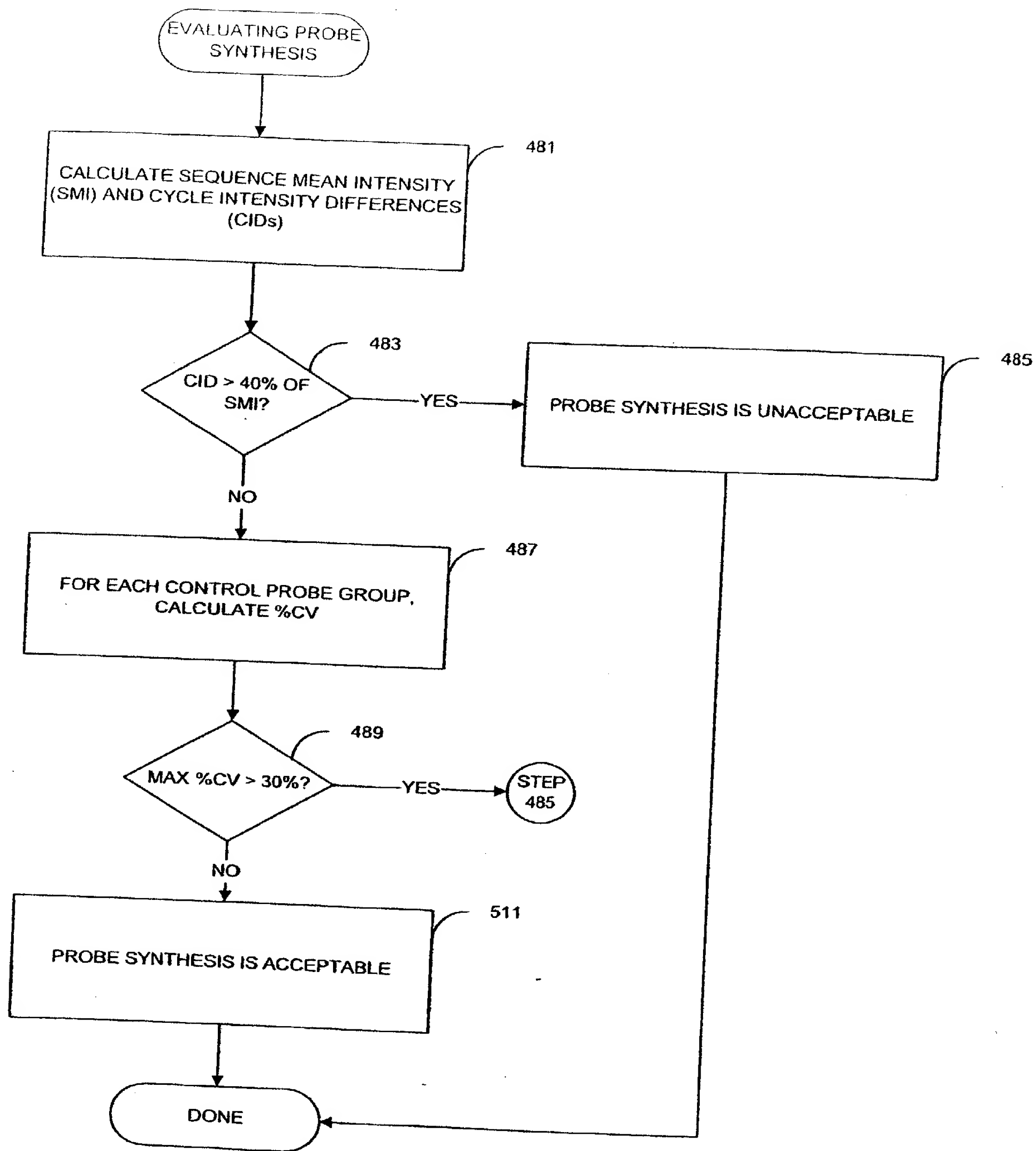


FIG. 13

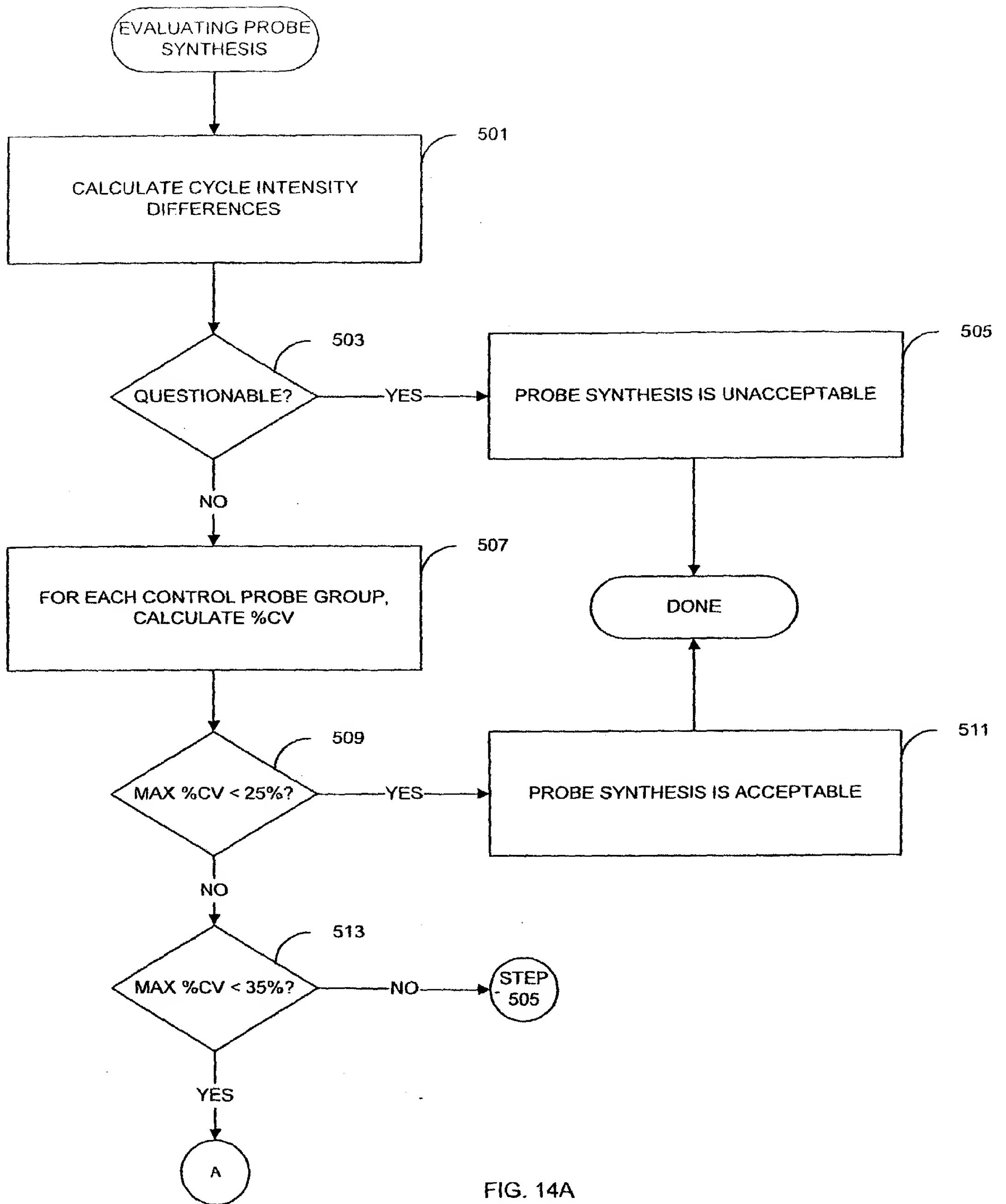


FIG. 14A

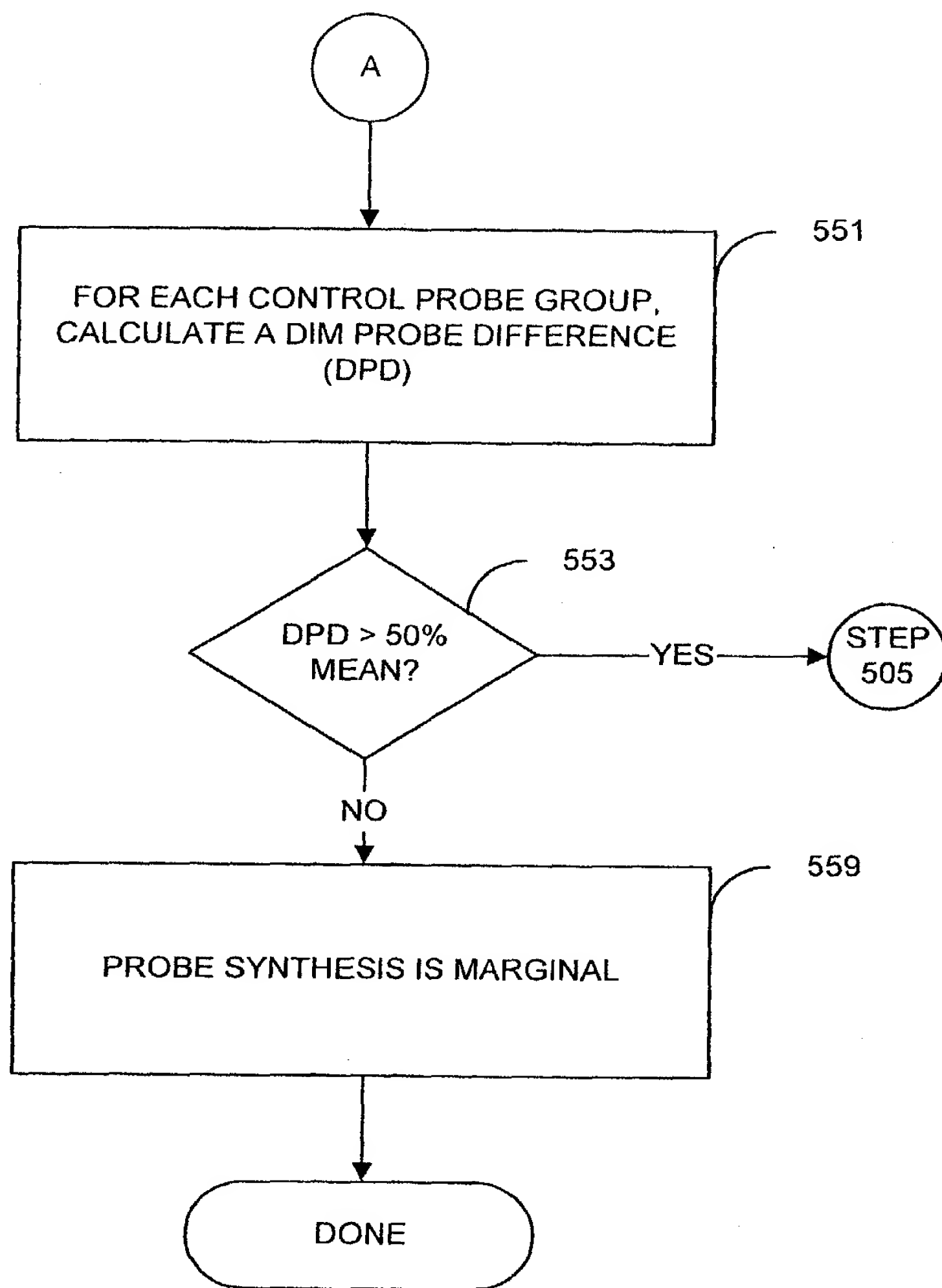


FIG. 14B